

10/14/03

EL 964691802 US

PATENT APPLICATION

Sheet 1 of 3

FORM PTO-1449		ATTY. DOCKET NO.	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		10004119-3		
(Use several sheets if necessary)		APPLICANT	Eric M. Rentschler, et al.	
		FILING DATE	GROUP	
		10/14/2003		

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
1L	1A	5,418,924	05/1995	Dresser	
↑	1B	5,522,064	05/1996	Aldereguia et al.	
↑	1C	5,701,438	12/1997	Bains	
	1D	5,727,005	03/1998	Le et al.	
	1E	5,809,340	09/1998	Bertone et al.	
	1F	5,974,499	10/1999	Norman et al.	
	1G	6,005,412	12/1999	Ranjan et al.	
	1H	6,065,132	05/2000	Takano, Toshiya	
	1I	6,125,078	09/2000	Ooishi et al.	
↓	1J	6,144,598	11/2000	Cooper et al.	
1L	1K	6,288,971 B1	09/2001	Kim	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
1L	1L	WO99/04494	01/28/1999	Intel Corporation		
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1L	1Q	"DDR SDRAM Registered DIMM Design Specification", IBM, March 2000 (62 pages)
1L	1R	"DDR SDRAM Module Serial Presence Detect Definitions", IBM, October 1999 (34 pages)
1L	1S	JEDEC Standard No. 79, "Double Data Rate (DDR) SDRAM Specification", June 2000 (72 pages)

EXAMINER	DATE CONSIDERED
H. Kim	1/29/06

FORM PTO-1449		ATTY. DOCKET NO. 10004119-3	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT Eric M. Rentschler, et al.		
(Use several sheets if necessary.)		FILING DATE 10/14/2003	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>lh</i>	2A	6,324,119	11/2001	Kim	
<i>A</i>	2B	6,338,113	01/2002	Kubo et al.	
	2C	6,370,630	04/2002	Mizuyabu et al.	
	2D	6,414,868	07/2002	Wong et al.	
	2E	6,424,198	07/2002	Wolford	
	2F	6,480,946	11/2002	Tomishima et al.	
	2G	6,530,001	03/2003	Lee, Jung-Keun	
	2H	6,532,525	03/2003	Aleksic et al.	
<i>N</i>	2I	6,553,450	04/2003	Dodd et al.	
<i>DR</i>	2J	6,625,702	09/2003	Rentschler, et al.	
<i>DR</i>	2K	6,633,965	10/2003	Rentschler et al.	

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		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	2L					
	2M					
	2N					
	2O					
	2P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

<i>lh</i>	2Q	"Preliminary Publication of JEDEC Semiconductor Memory Standards-DDR SDRAM Specification", August 1999 (73 pages)
	2R	
	2S	

EXAMINER

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DATE CONSIDERED

1/29/06

FORM PTO-1449		ATTY. DOCKET NO. 100041193	APPLICATION NO.	CONFIRMATION NO.
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		APPLICANT Eric M. Rentschler, et al.		
(Use several sheets if necessary)		FILING DATE 10/14/2003	GROUP	

REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
1d	3A	2001/0003837	06/2001	Norman et al.	
↑	3B	2001/0046163	11/2001	Yanagawa, Miki	
	3C	2001/0054135	12/2001	Matsuda	
✓	3D	2002/0147896	10/2002	Rentschler et al.	
wh	3E	2002/0172079	11/2002	Hargis et al.	
	3F				
	3G				
	3H				
	3I				
	3J				
	3K				

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	3L					
	3M					
	3N					
	3O					
	3P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	3Q	
	3R	
	3S	

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1/29/06